

Configurable Glueless Microprocessor Interface

ABSTRACT OF THE DISCLOSURE

A host control interface for use in interfacing an external host processor with
5 internal control/status registers of an integrated circuit is provided. In accordance
with the teachings of the present invention, the control interface selectively couples
the integrated circuit with an interchangeable one of a variety of host processor
types. In one embodiment, the control interface supports processors having a
multiplexed address/data port as well as processors having separate address and
10 data ports. Similarly, in one embodiment, the control interface supports processors
utilizing a transfer start indication signal in cooperation with a read/write signal, as
well as processors utilizing separate read/write strobes.